	Application No.	Applicant(s)
Notice of Allowability		
	10/639,464 Examiner	MURATA ET AL.  Art Unit
	Lammer	Artonic
	Tuan T. Dinh	2841
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>06/29/06</u> .		
2. The allowed claim(s) is/are <u>8-14 (renumber claims are 1-7)</u> .		
<ul> <li>3.</li></ul>		
Attachment(s)  1. Notice of References Cited (PTO-892)  2. Notice of Draftperson's Patent Drawing Review (PTO-948)  3. Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date  4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. ☐ Notice of Informal P 6. ☐ Interview Summary Paper No./Mail Dat 7. ☐ Examiner's Amendn 8. ☑ Examiner's Stateme 9. ☐ Other	(PTO-413), e

## **DETAILED ACTION**

Claims 1-7 are canceled in the Response filed on 06/29/06, and claims 8-14 are pending.

Claim 8 is amended in the response filed on 06/29/06 to read on figure 1 of the elected Specie.

Claims 8-14 are directed to an allowable product, previously withdrawn from consideration as a result of a restriction requirement, claims 8-14 are hereby rejoined and fully examined for patentability under 37 CFR 1.104.

## Allowable Subject Matter

1. Claims 8-14 are allowed (renumber claims are 1-7).

The following is an examiner's statement of reasons for allowance: the references cited disclose a printed circuit board (PCB) assembly comprising a connector having a plurality of pins, first and second printed circuit boards connected to the connector, each having a signal line and a plurality of lines, lossy elements, and some other claimed elements. However, they do not disclose or suggest in combination of each of the first and second printed-circuit boards having the plurality of lines connected to open pins of the connector but not be connected to any signal lines; and lossy elements on at least one of the first and second printed-circuit boards connected to respective open pins of the connector by respective lines.

Art Unit: 2841

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan T. Dinh whose telephone number is 571-272-1929. The examiner can normally be reached on M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kammie Cuneo can be reached on 571-272-1957. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

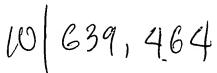
Art Unit: 2841

Tuan Dinh August 28, 2006.



Fig.1

## Replacement Sheet





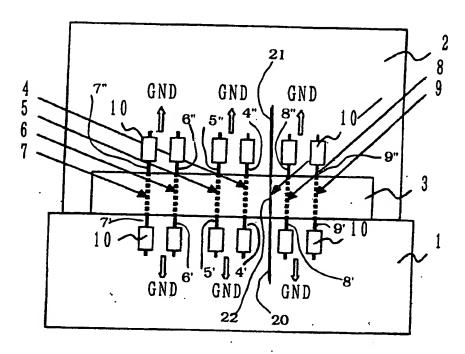


Fig.2

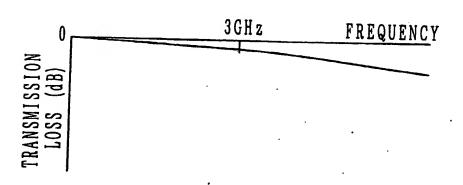


Fig.3

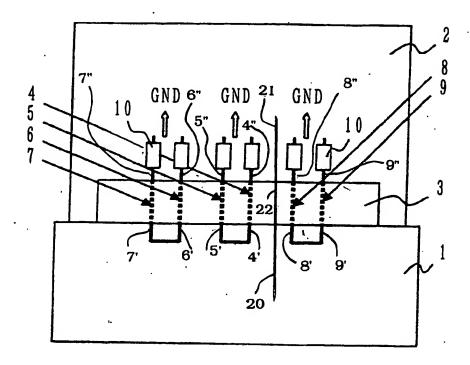


Fig.4

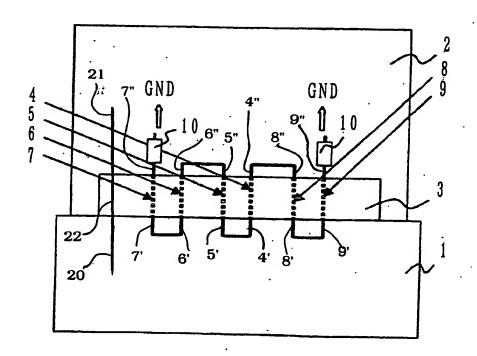


Fig.5

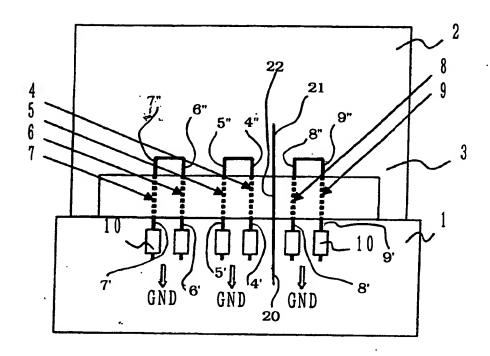


Fig.6

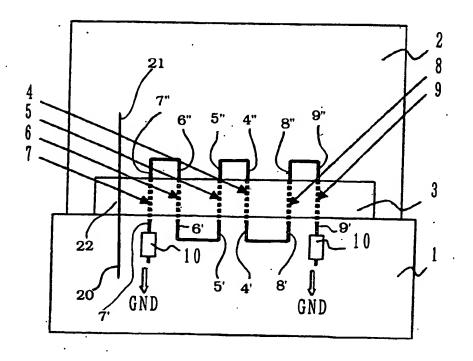


Fig.7

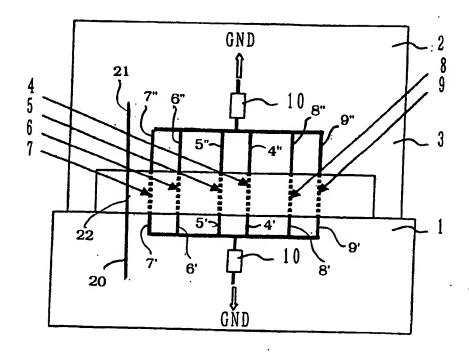


Fig.8

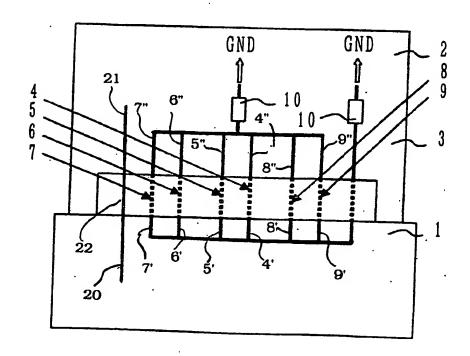


Fig.9

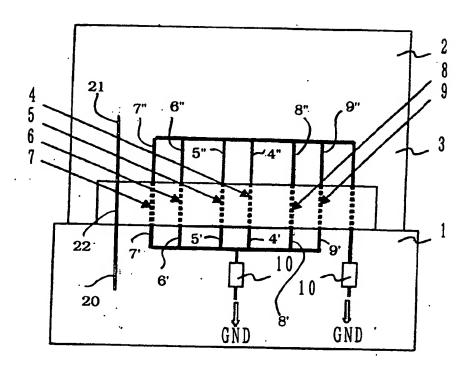


Fig.10

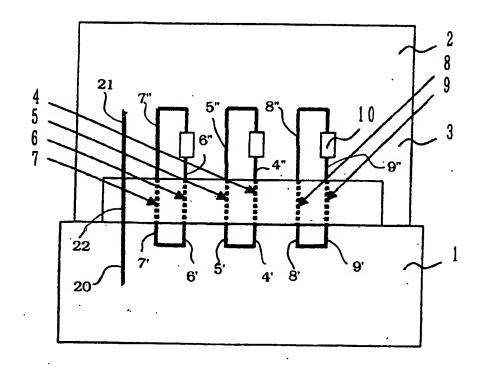


Fig.11

